

# External Awards

## **IEICE ELEX Best Paper Award in the Year 2012**

**Winners:** Yoshio Takahashi<sup>†1</sup> and Tsutomu Matsumoto<sup>†2</sup>

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**Organization:** The Institute of Electronics, Information and Communication Engineers (IEICE)

For “A Proper Security Analysis Method for CMOS Cryptographic Circuits”.

Differential Power Analysis (DPA) aims at revealing secret keys in cryptographic devices by analyzing their power consumption as side-channel information. Although power consumption models based on transition probability were used to evaluate DPA resistance in previous studies, the adequacy of these models has not been adequately confirmed. In this paper, we describe two experiments on obtaining precise information of power consumption, and show that Random Switching Logic, one of the DPA countermeasures, is in reality not secure against DPA.

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